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PATENT NUMBER.

U.S. **UTILITY** Patent Application

O.I.P.E.	PATENT DATE
AC SCANNED <u>PD</u> Q.A. <u>TC</u>	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/712749		029	830	37281 37281	DAVID TUAN NGUYEN

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ABANDONED

Low profile integrated module interconnects and method of fabrication

PTO-2040
12/89

ISSUING CLASSIFICATION

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED		
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____				ISSUE FEE		
	(Assistant Examiner)			(Date)	Amount Due	Date Paid
	(Primary Examiner)			(Date)	ISSUE BATCH NUMBER	
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.				/ Local Instruments Examiner _____ (Date)		

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